



BAŞKENT UNIVERSITY
ELECTRIC & ELECTRONICS ENGINEERING

EEM 214 ~ ELECTRONICS–I
LABORATORY
REPORT

Experiment Name : Diode Clipping & Clamping Circuits

Experiment Number : 5

Student Name : [REDACTED]

Student Number [REDACTED]

Experiment Date/Day: 7 / 4 / 2006
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Laboratory Number:

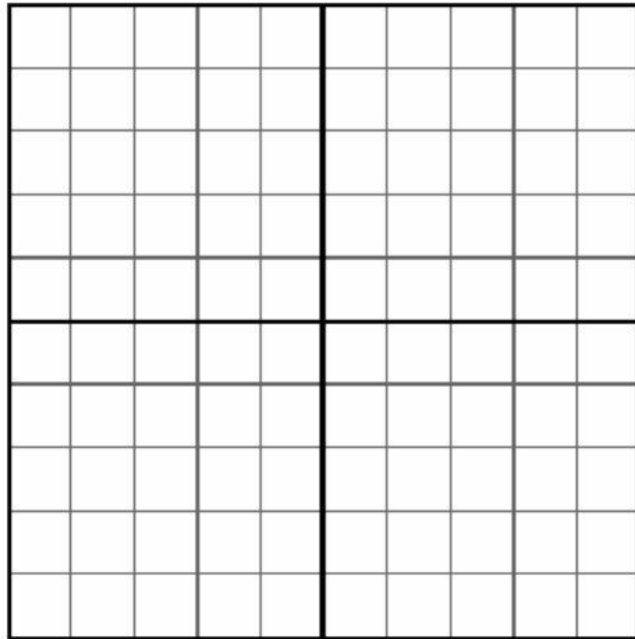
Honor Pledge:

I did this preliminary work or lab report by myself and I have neither cheat from anybody nor let anybody cheat from me at the any part of this work.

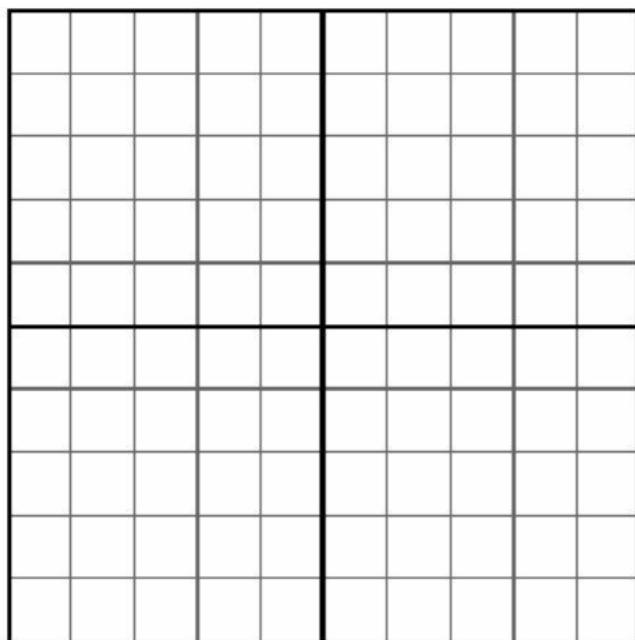
Student Signature:

.....

1-)
a-)

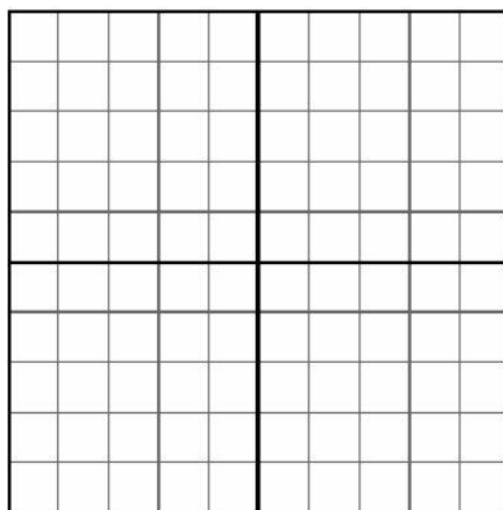
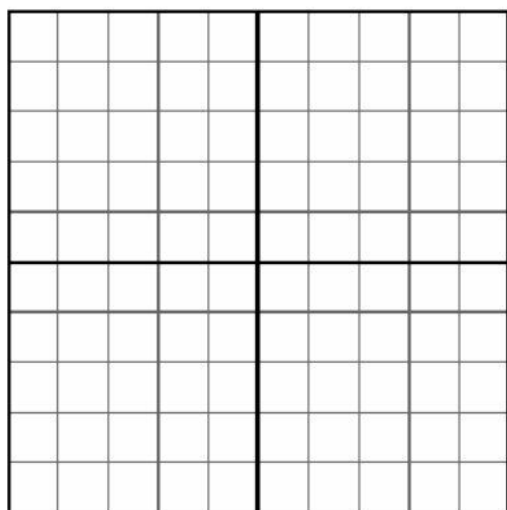
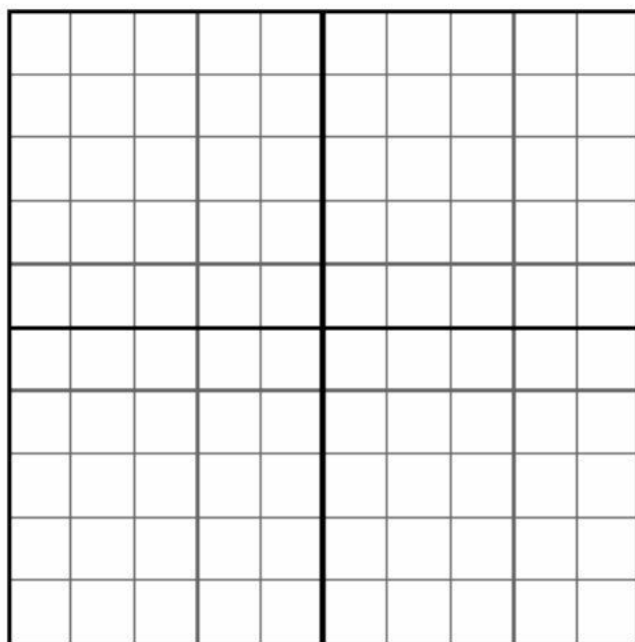


b-)

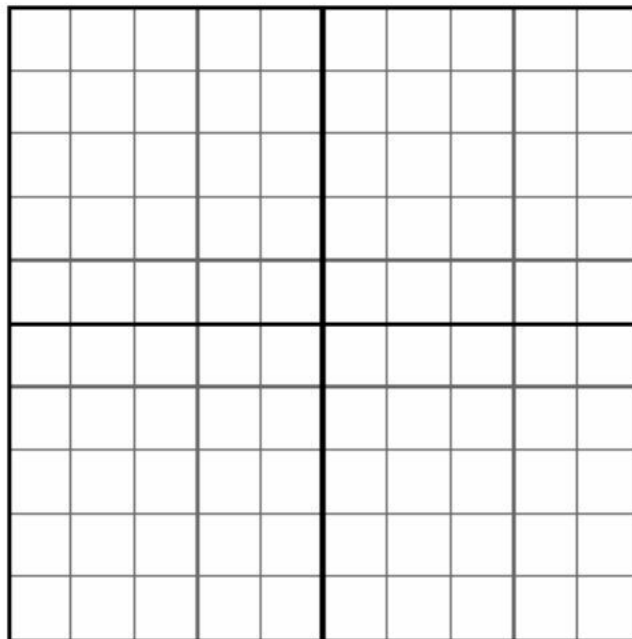
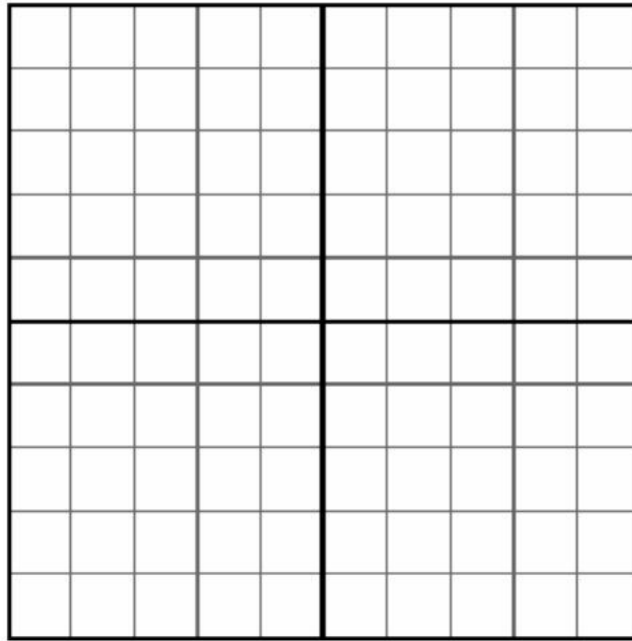


When diode and DC source reversed, diode is on for the values of $V_{in} < -5.7 \text{ V}$. But when it is not reversed, diode is on for the values of $V_{in} > 5.7 \text{ V}$.
This values are sum of the diode and DC source voltages.
 $0.7 + 5 = 5.7 \text{ V}$ for a part
 $-0.7 - 5 = -5.7 \text{ V}$ for b part

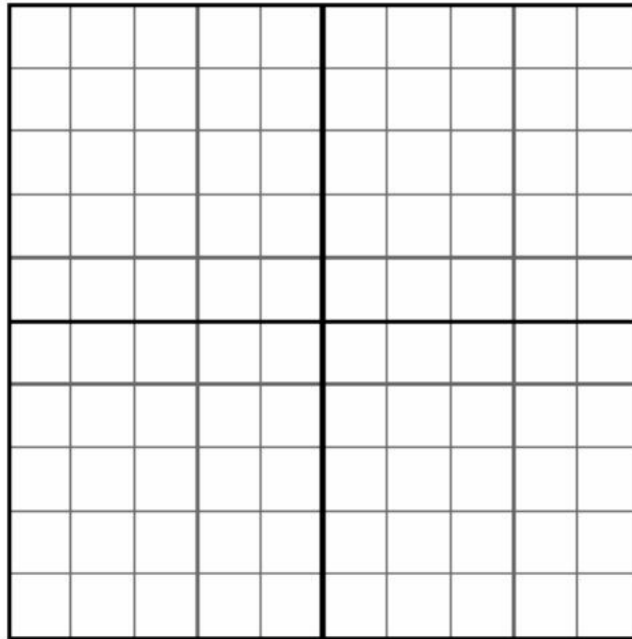
2-)
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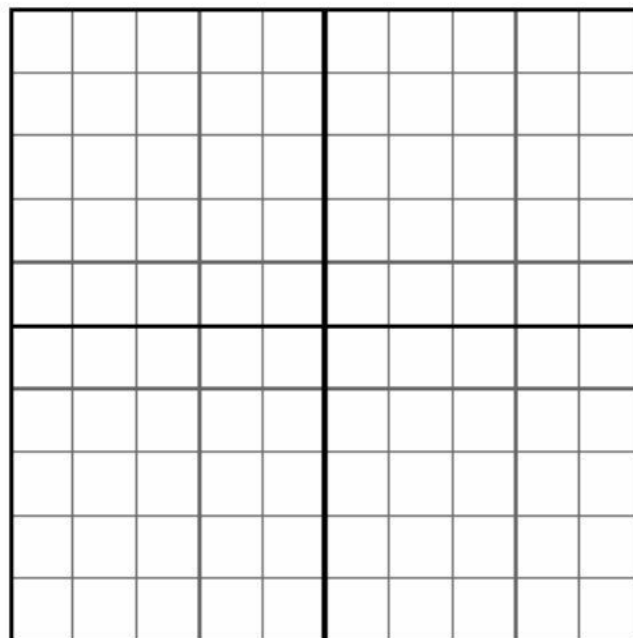
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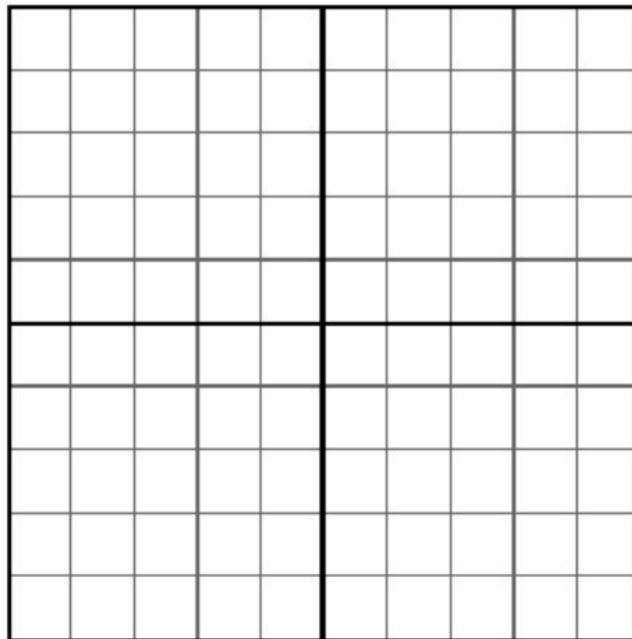
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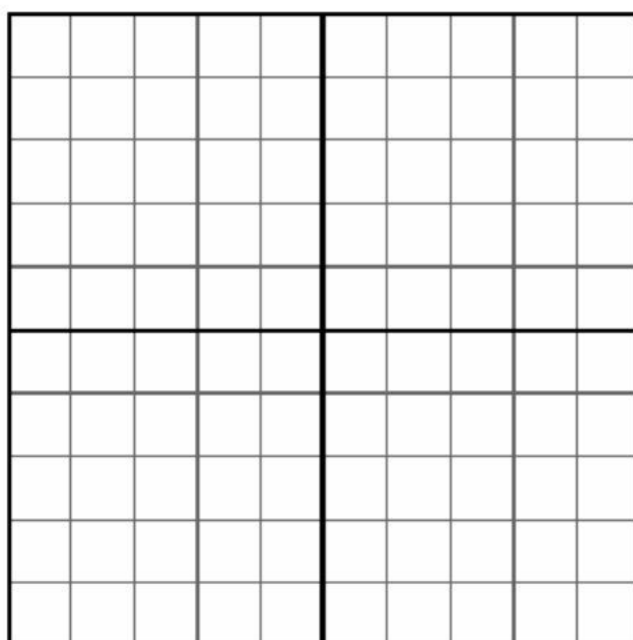
4-)
a-)



5-)



6-)
a-)



b-)

